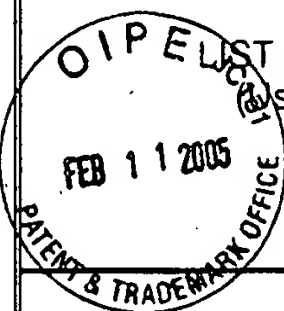


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U.S. PATENT DOCUMENTS

*Examiner Initial		Document Number	Date	Name	Class	Sub-class	Filing Date If Appropriate
<i>gll</i>	AA	6,121,133	09-2000	Iyer et al.	438	636	
	AB						

FOREIGN PATENT DOCUMENTS

		Document Number	Date	Country	Class	Sub-class	Translation	
							Yes	No
	AM							
	AN							

OTHER REFERENCES (including Author, Title, Date, Pertinent Pages, Etc.)

<i>JR</i>	AR		Wolf et al., Silicon Processing for the VLSI Era, 1986, Lattice Press, Volume 1, Pages 1 and 2.					
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